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Application Number

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Filing Date

9/19/2003

First Named Inventor

Cho

Art Unit

2818

Examiner Name

Tran, Mai Huong C

Attorney Docket Number

C002-D-2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MH		Nemati, Farid and Plummer, James, D., A Novel High Density, Low Voltage SRAM Cell with a Vertical NDR Device, VLSI Technology Technical Digest, 6/1998	
MH		Nemati, Farid and Plummer, James, D., A Novel Thyristor-based SRAM Cell (T-RAM) for High-Speed, Low-Voltage, Giga-scale Memories, International Electron Device Meeting Technical Digest, 1999	
MH		Gribnikov, Z.S., Korobov, V.A., and Mitin, V.V., The Tunnel Diode as a Thyristor Emitter, Solid-State Electronics, Vol. 42, No. 9, 1998, pp. 1761-1763	

Examiner Signature	<i>Mai Huong C</i>	Date Considered	09/01/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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